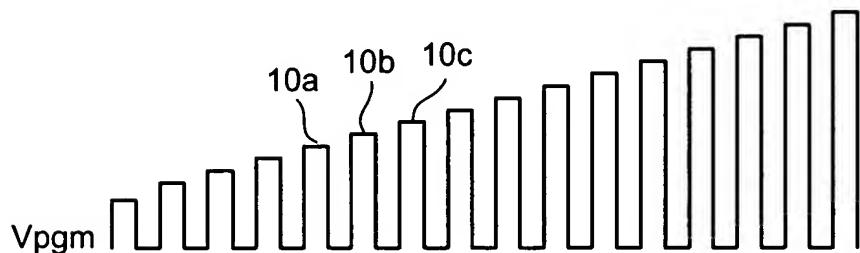


Title: EFFICIENT VERIFICATION FOR COURSE/FINE PROGRAMMING OF NON-VOLATILE MEMORY  
 Applicants: Guterman, et al. Docket No.: SAND-01011US0  
 Appl. No.: Unknown Atty: Burt Magen  
 Filing Date: January 27, 2004 Phone: (415) 369-9660  
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Fig. 1



# of cells

Fig. 2

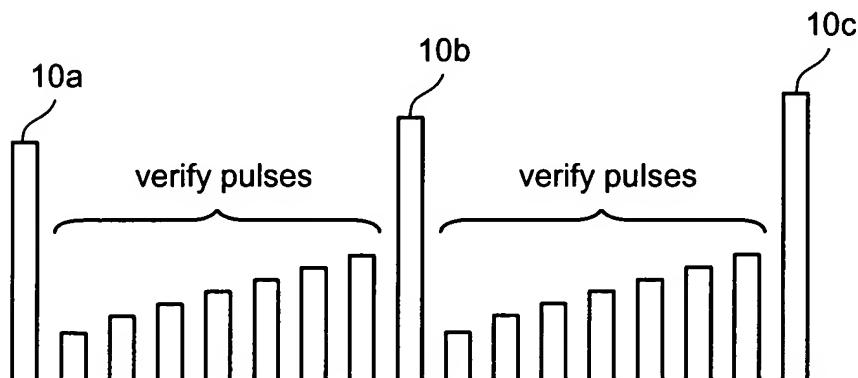
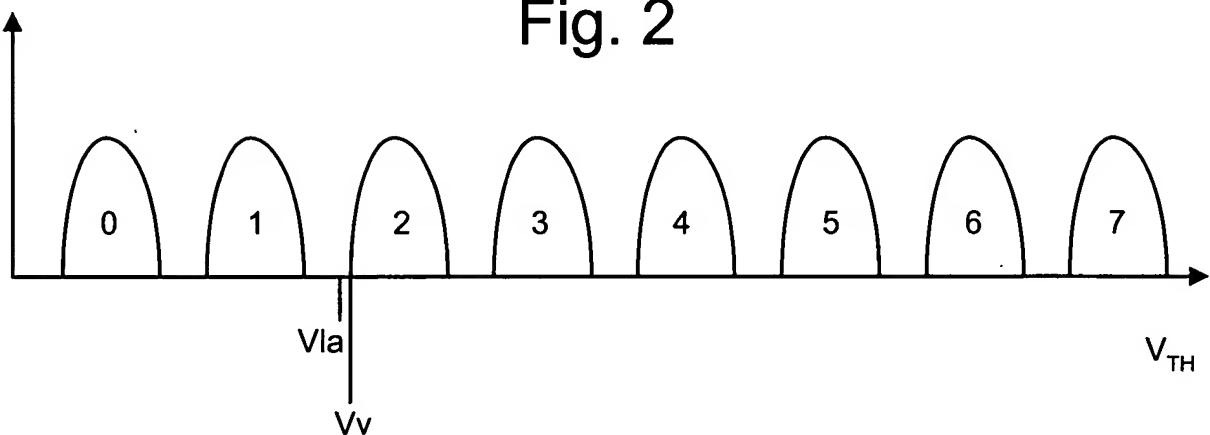


Fig. 3

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Fig. 4

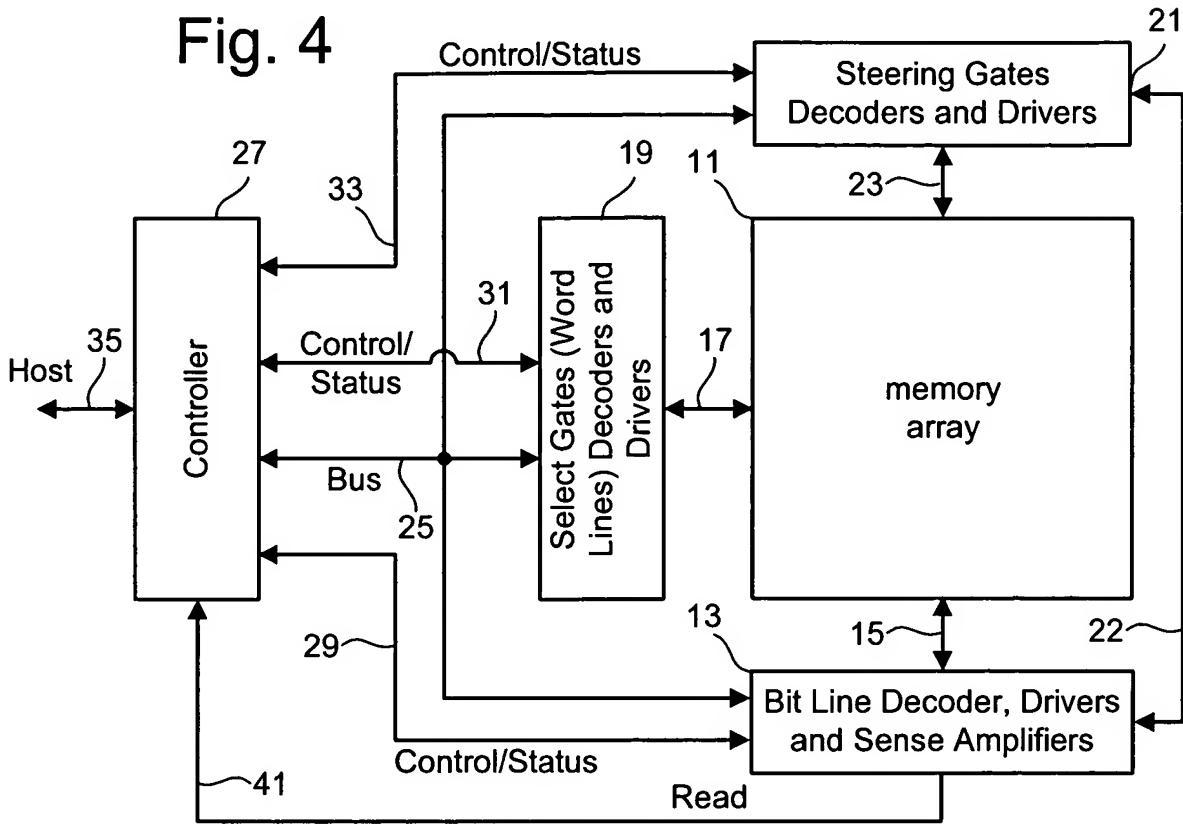
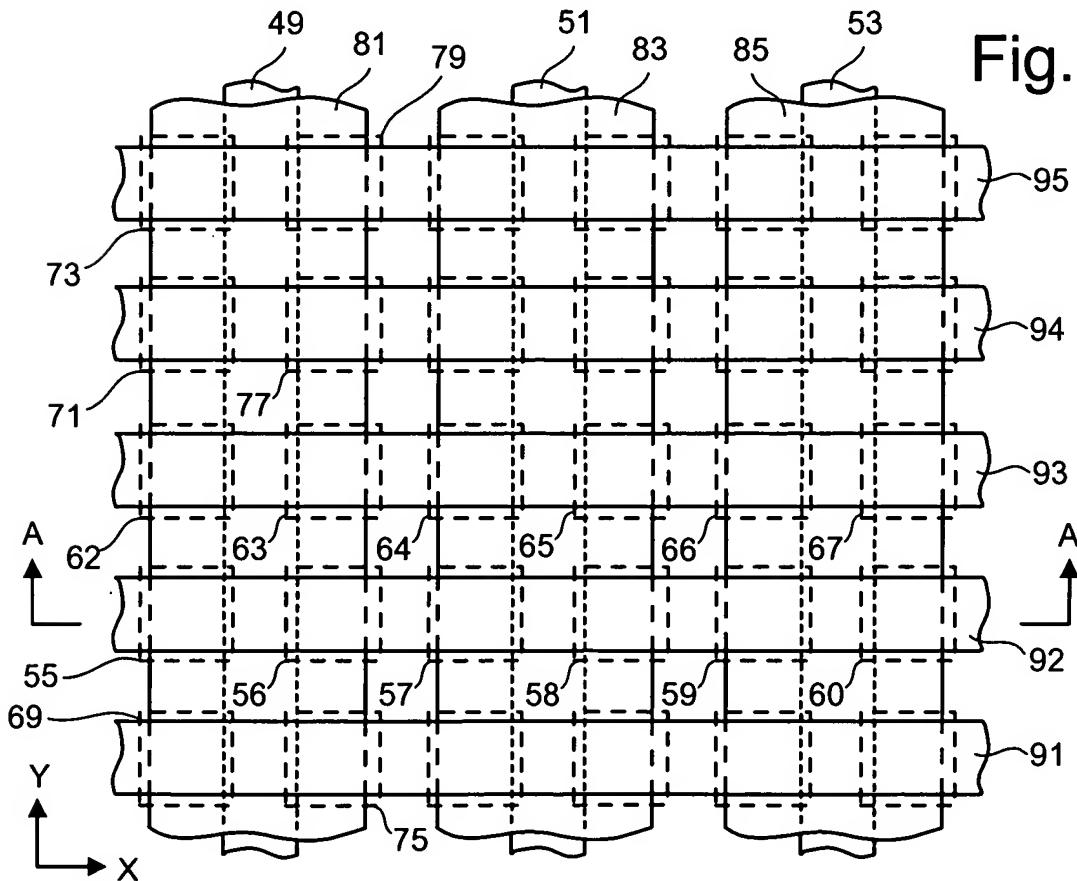


Fig. 5



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Fig. 6

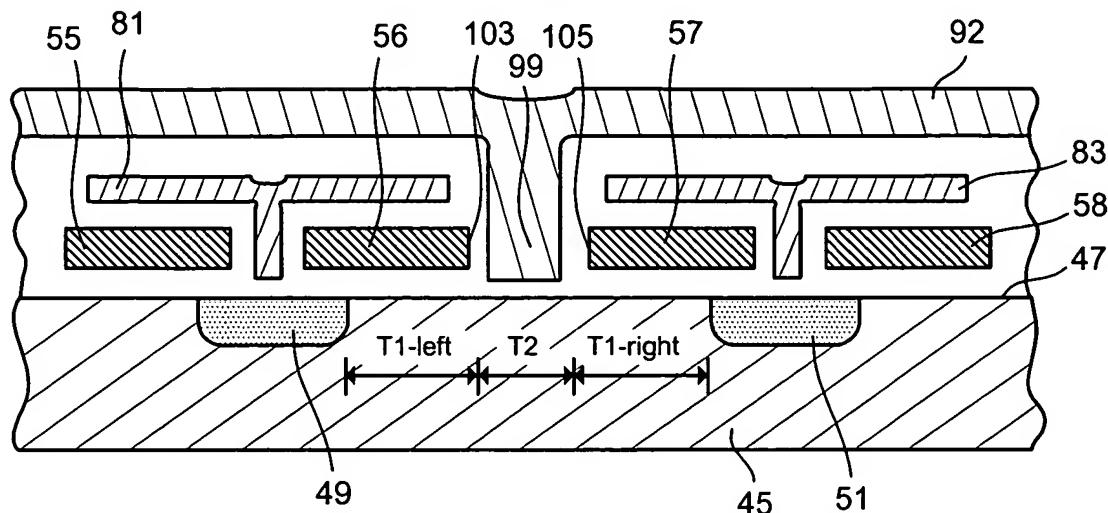
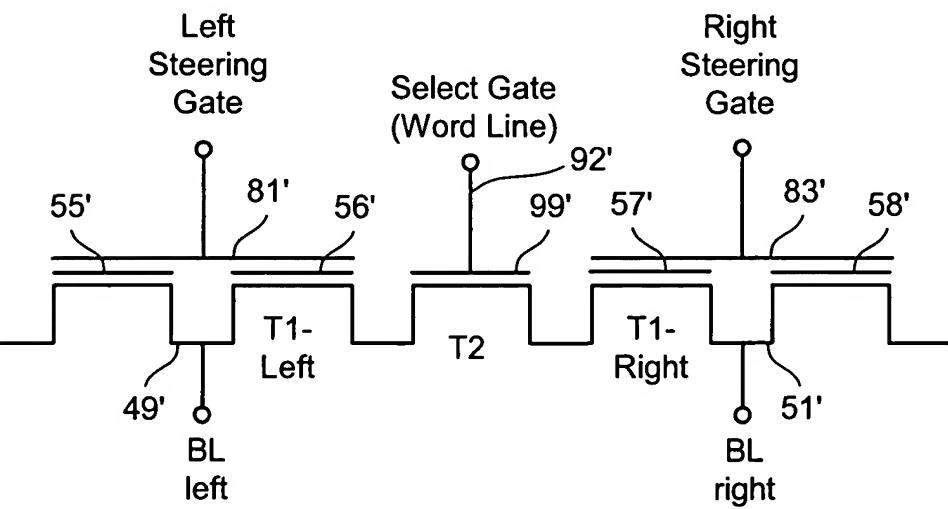


Fig. 7

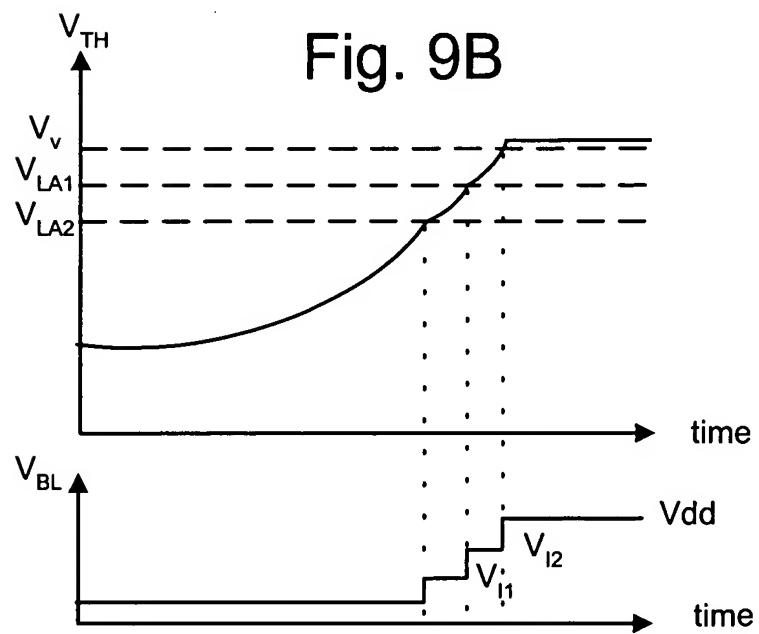
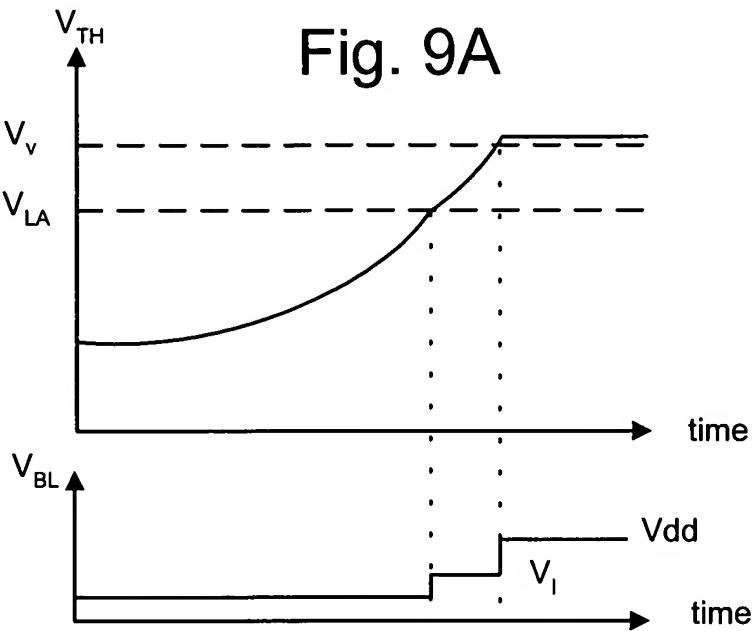


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Fig. 8

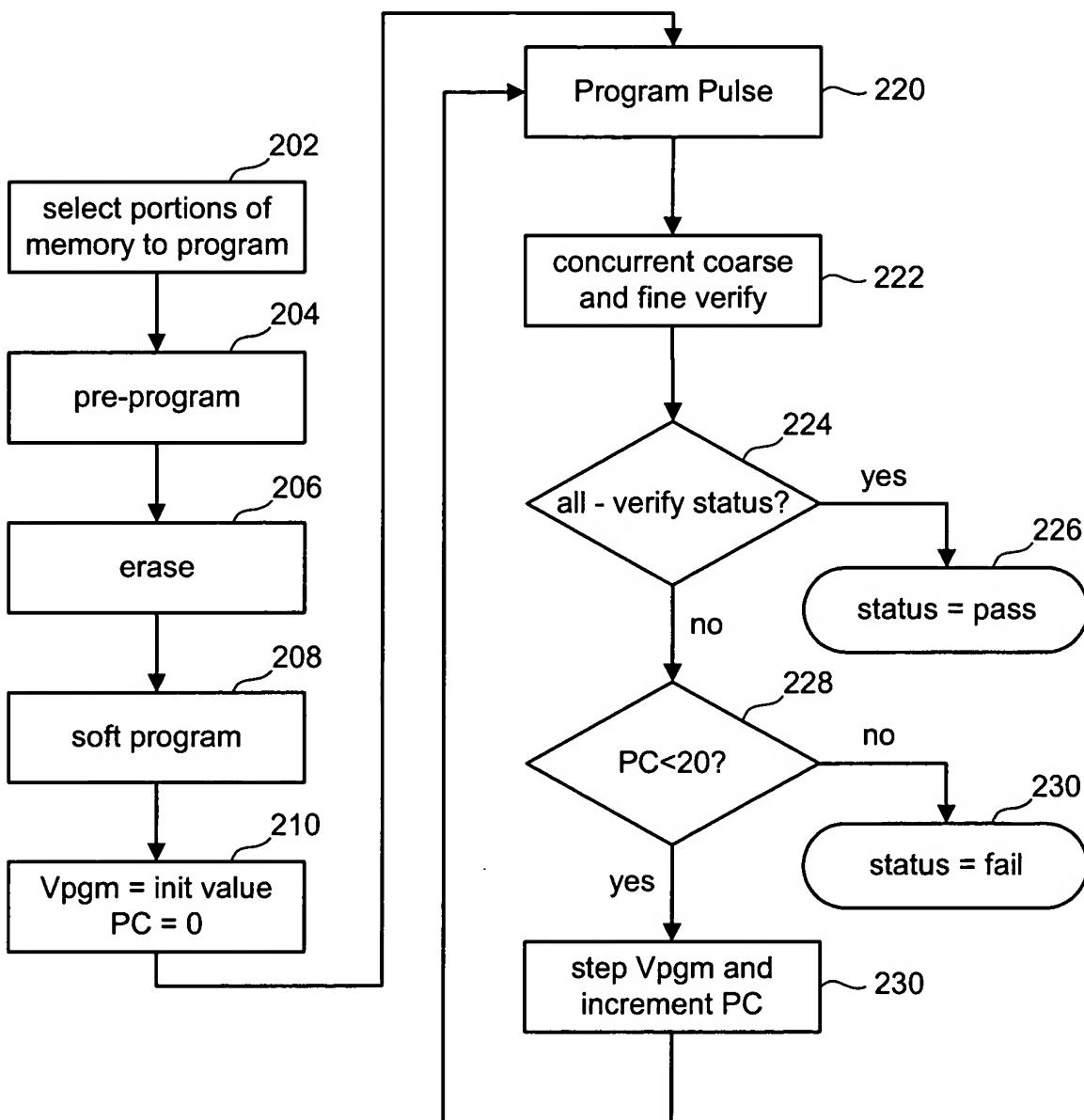
FUNCTION BEING PERFORMED ON CELL	SELECT GATE (WORD LINE)	LEFT BIT LINE (BL - LEFT)	LEFT STEERING GATE	RIGHT STEERING GATE	RIGHT BIT LINE (BL-RIGHT)
(1) UNSELECTED ROW (2) ERASE (TO WORD LINE)	0 $V_E$	X 5 $V_{SR}$	X 0 $V_M$	X 0 $V_{BR}$	X 5 1
(3) READ LEFT FLOATING GATE (4) READ RIGHT FLOATING GATE	$V_{SR}$	0 1 $V_{SP}$	$V_{BR}$ 5 $V_P$	$V_M$ $V_{BP}$ $V_P$	0 0 5
(5) PROGRAM LEFT FLOATING GATE (6) PROGRAM RIGHT FLOATING GATE	$V_{SP}$	0 0 $V_{SP}$	$V_{BP}$ X X	X X X	0 0 5
(7) NO PROGRAM IN SELECTED ROW		5			
(8) ERASE (TO CHANNEL) [WITH VOLTAGES OF BOTH THE p-well AND n-well EQUAL TO $V_E$ , AND THE SUBSTRATE AT ZERO VOLTS]	$V_{SE}$	FLOAT	0	0	FLOAT

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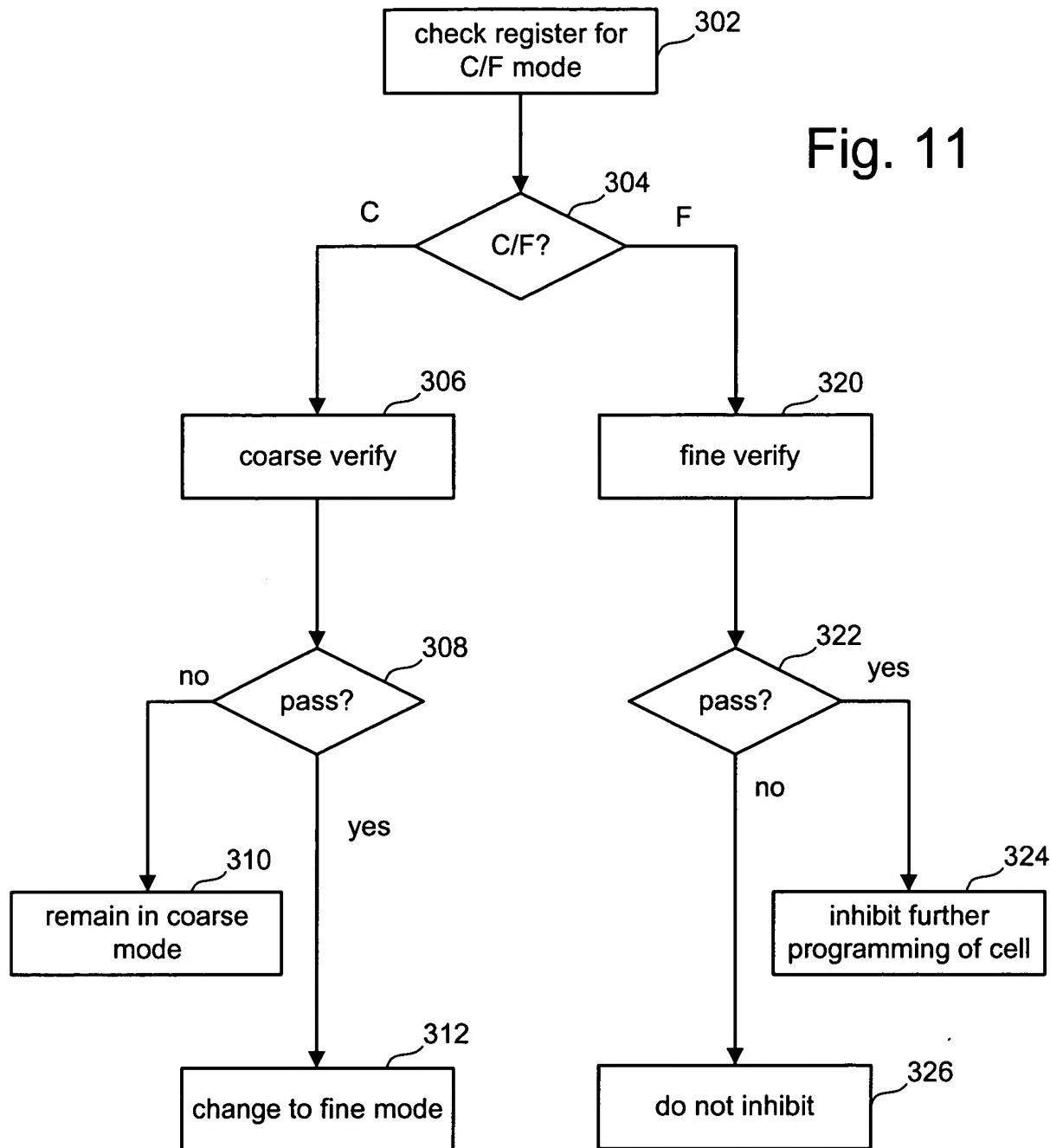


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Fig. 10



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Bit Line

Fig. 12

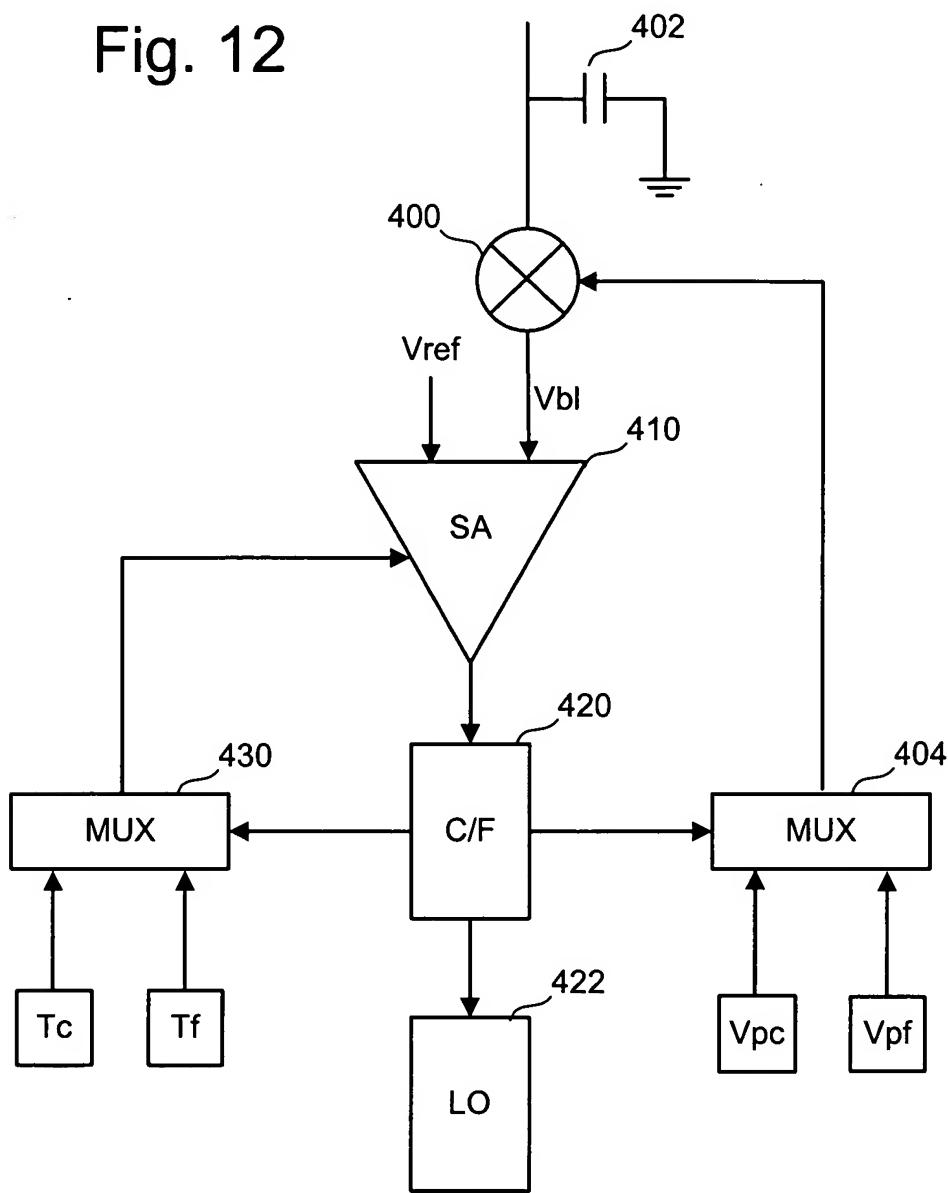
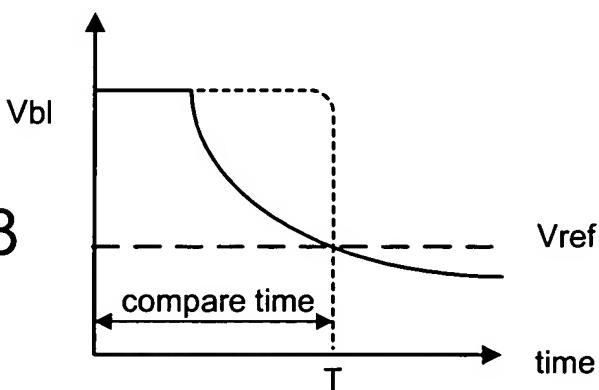


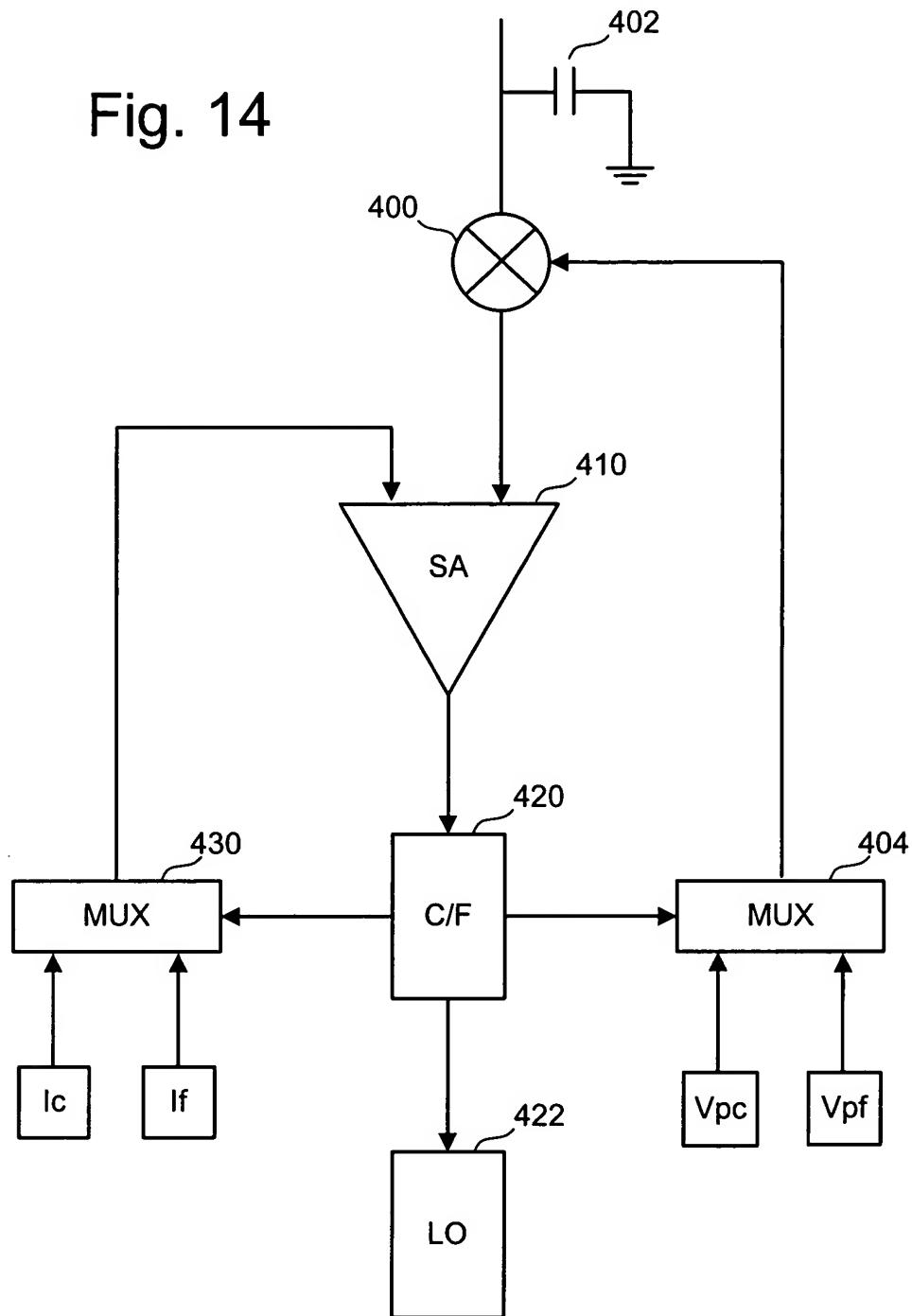
Fig. 13



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Bit Line

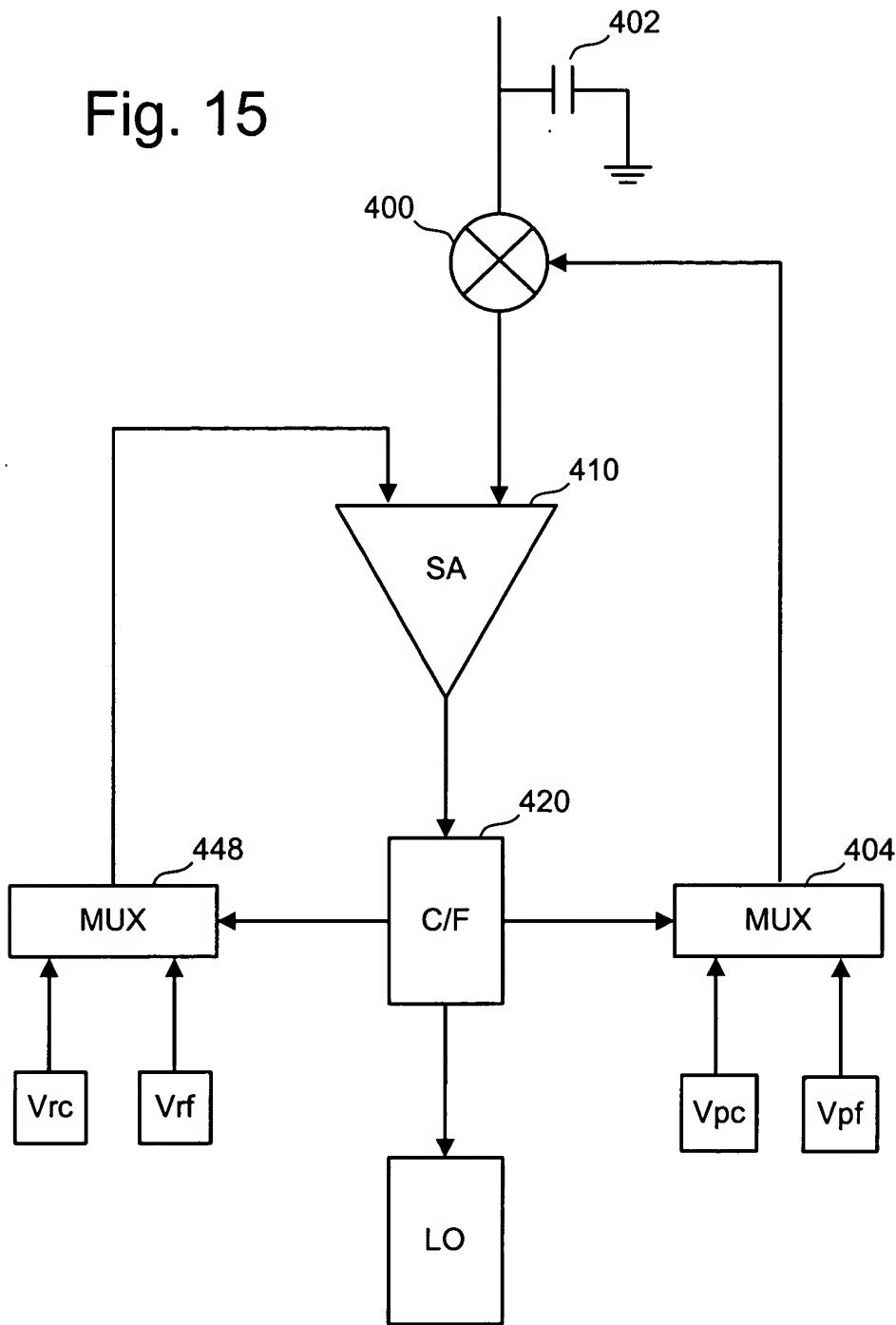
Fig. 14



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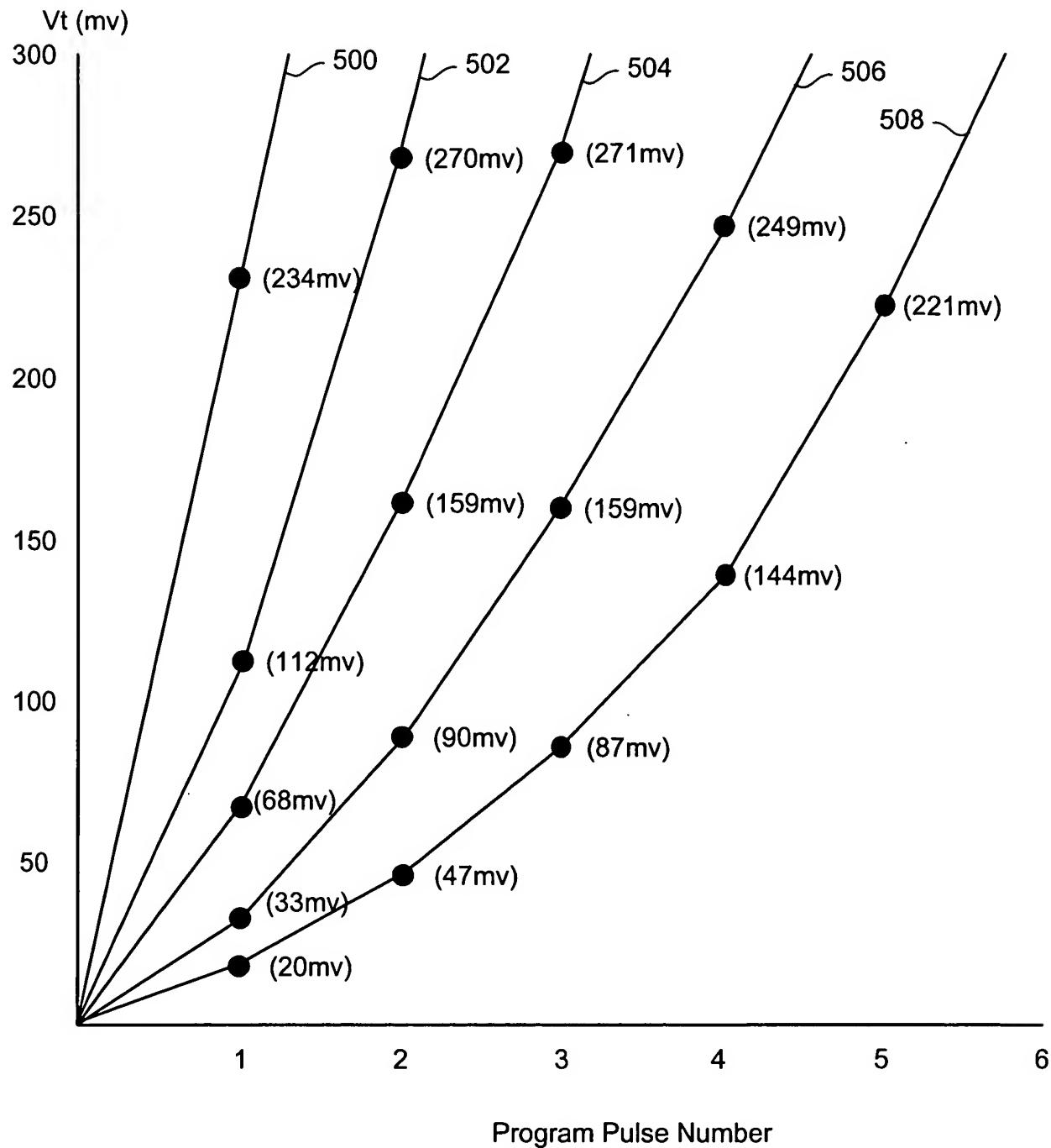
Bit Line

Fig. 15



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Fig. 16



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Fig. 17

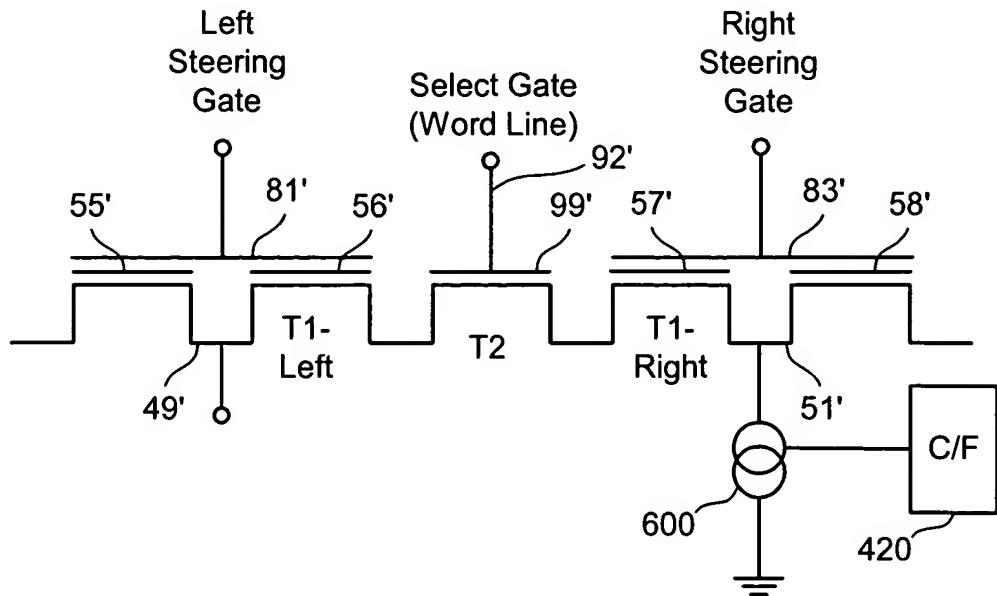
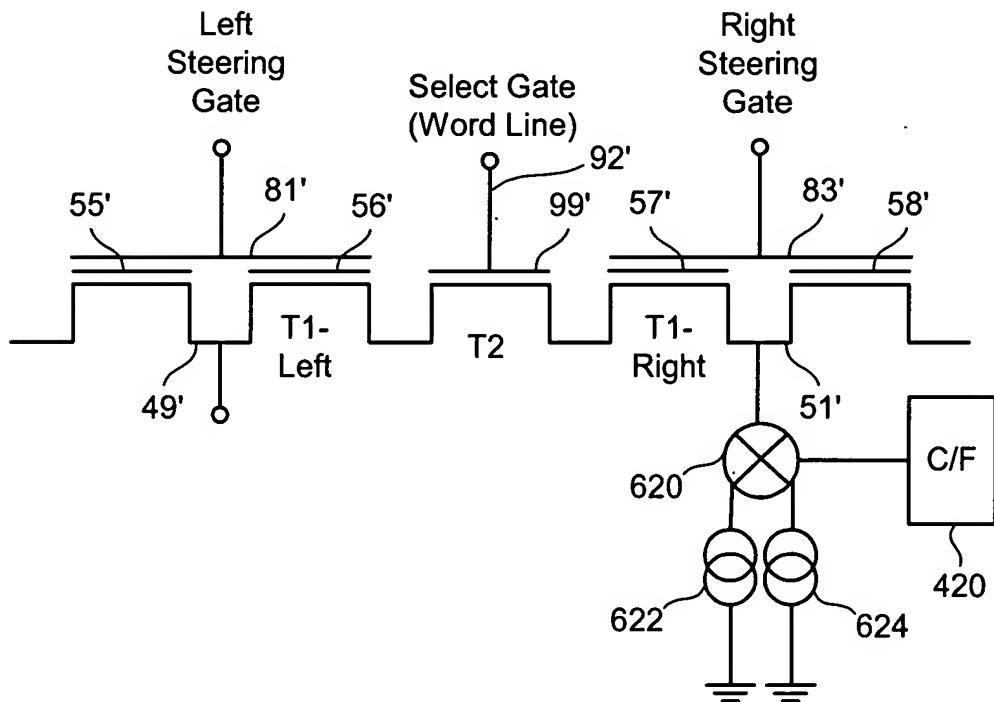


Fig. 18



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Fig. 19

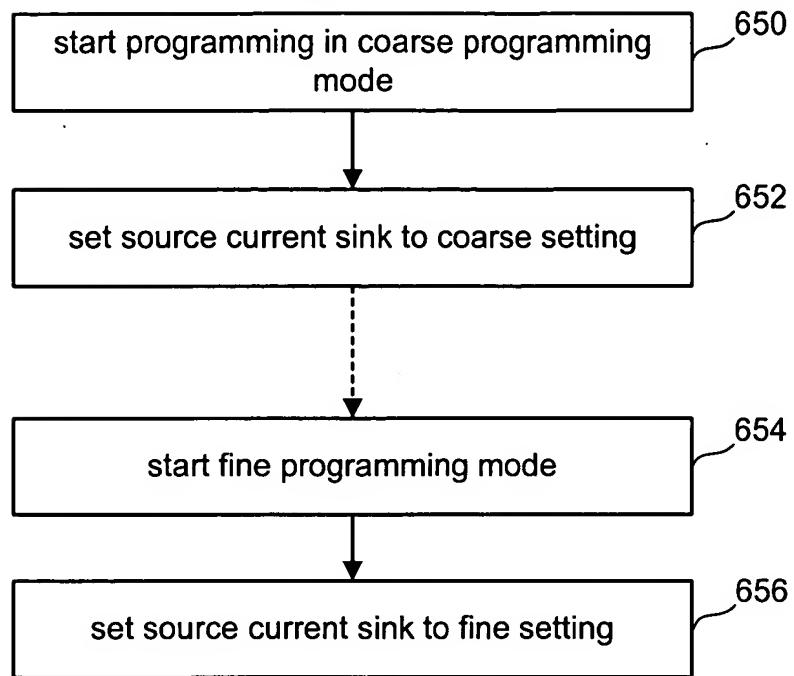
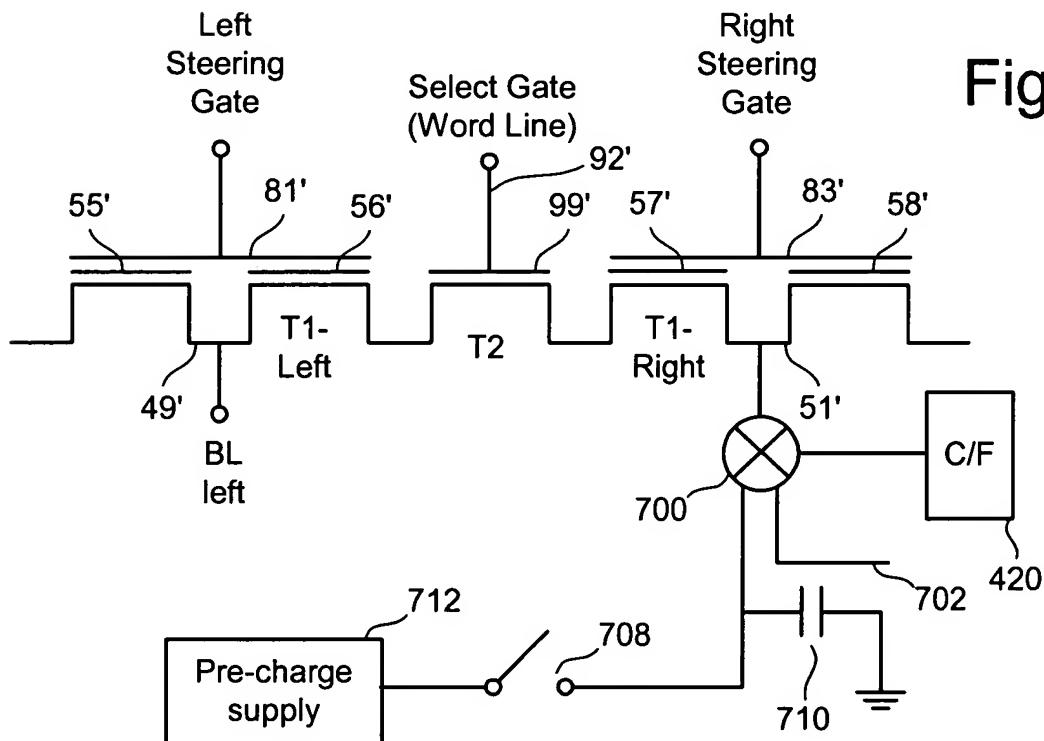


Fig. 20



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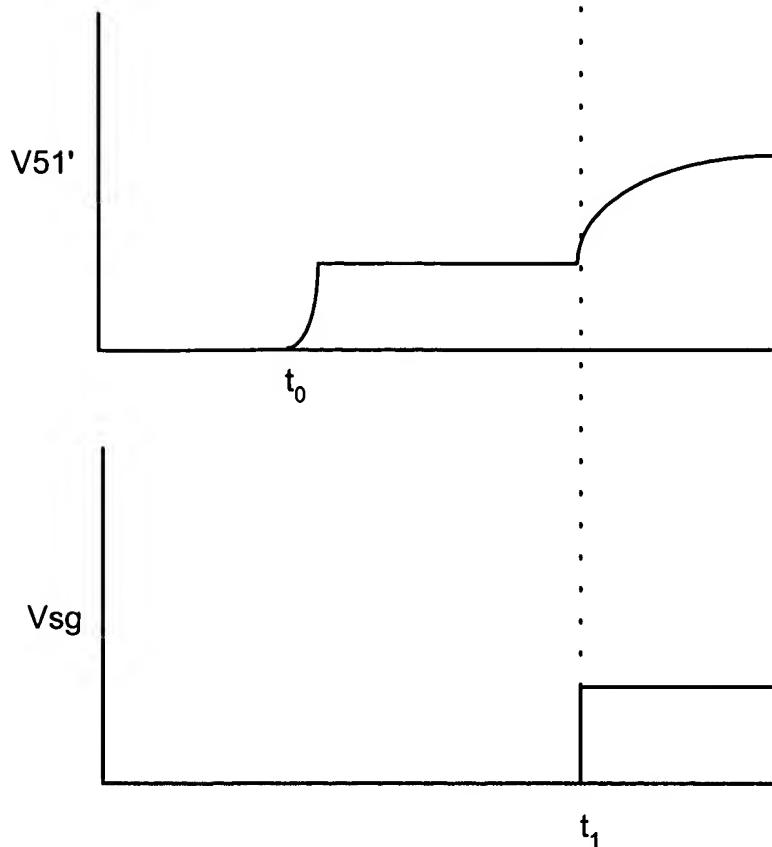


Fig. 21

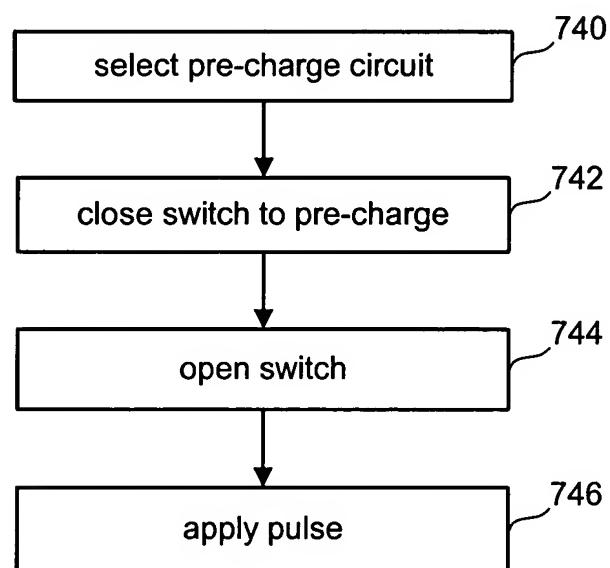


Fig. 22

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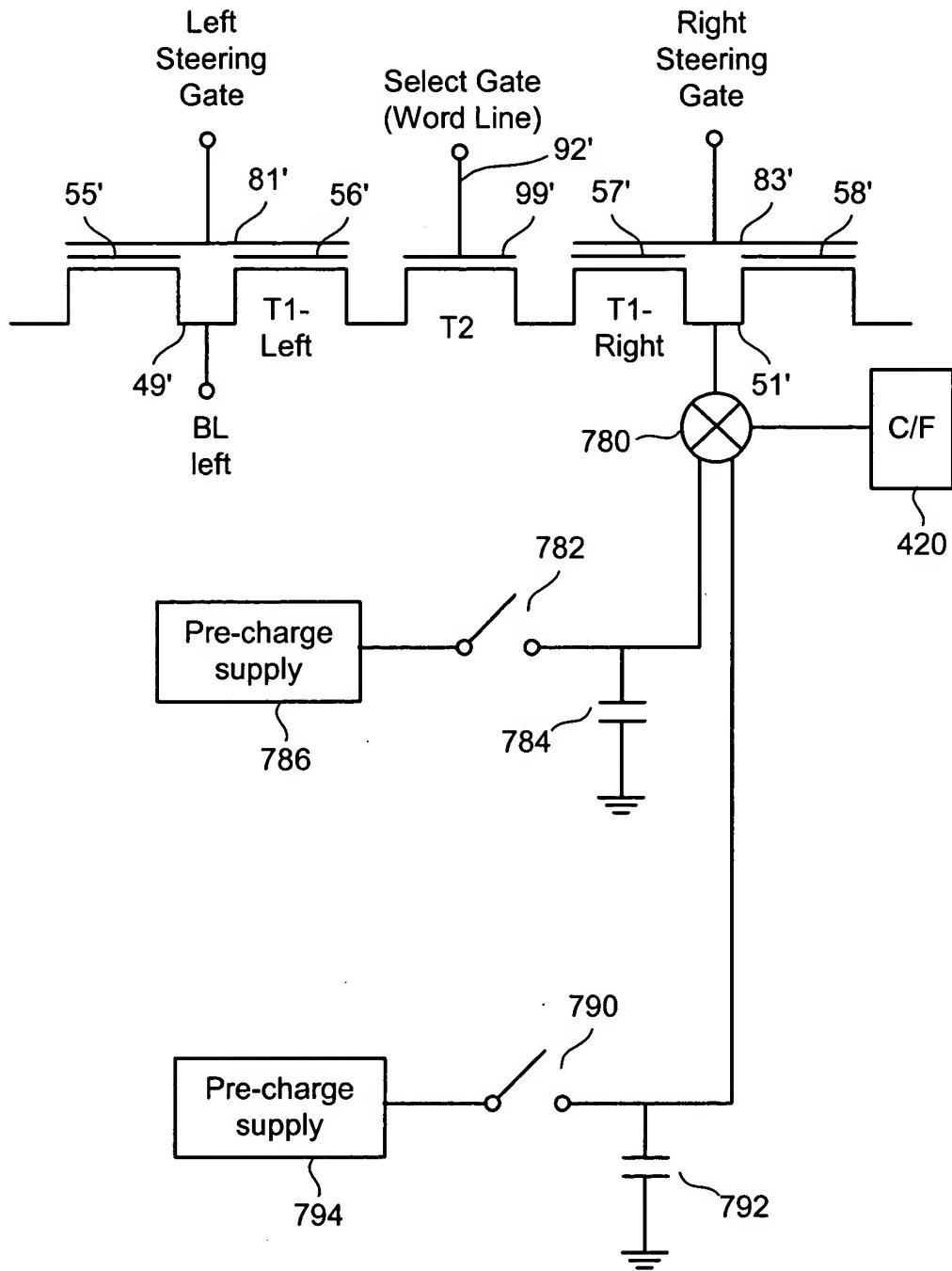


Fig. 23

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